

Saurav Mandal

HOD, Department of Electronics and Communication Engineering
DIATM, Durgapur, West Bengal
M-7076201097

Professional Experience

2011-Current : Assistant Professor and Head of the Department in the Department of Electronics & Communication Engineering.

Organisation : Durgapur Institute of Advanced Technology and Management, AICTE and MAKAUT approved.

Subject Thought: Current Semester I have taken Digital Signal Processing and VLSI design.

2007-2009 : Nokia Siemens Networks

Responsibilities: i) Operation and Maintenance Head of Backbone Networks of Vodafone Essar South Ltd Kolkata to Siliguri.

ii) BSC and Radio Transmission equipment maintenance.

iii) Worked as a team leader of Azadpur BSC, Malda BSC, Buniadpur BSC, Gangarampur BSC, Balurghat BSC.

Academic Qualification

2016-Current: **PhD(Ongoing)**, Under the supervision of Dr.Kaushik Ghosh

Research Topic: Detection of Nonlinearity and complexity in ECG signal for Arrhythmia and Design Analytic Modeling for Predicting the Future Trends.

2009-2011: **Master of Technology (M.Tech)**, Microelectronics and VLSI Technology.

Dissertation Topic: On the design of high performance reconfigurable DSP processor using FPGA.

Submitted on: 15 May, 2011

Supervisor: Prof. Amitabha Sinha

2003-2011: **Bachelor of Technology (B.Tech)**, Electronics and Communication Engineering, Durgapur.

1999-2002 : Higher Secondary passed in year 2002 and Madhyamik passed in year 2000 with 1st division.

Membership and Professional Body

1. Institute of Engineers India Associate Membership
2. Advisor of IEI Students Chapter

Publications

1. Saurav Mandal, Ashis Kumar Mal "Analysis of resistive load ring oscillator" IEEE 2nd international conference at JU, ReTIS 2015
2. Manisha Ghosh, Saurav Mandal " Architecture of high performance reconfigurable DSP processor " International Journal published
3. Saurav Mandal, Ashis Kumar Mal "On the design of high performance reconfigurable DSP processor using FPGA" International Journal published